



Reason of the resolution

MASTER'S DEGREE IN SEMICONDUCTOR ENGINEERING AND MICROELECTRONIC DESIGN (SEMD)

Centre/School

Barcelona School of Telecommunications Engineering

Academic course
2025/2026

Semester
Q1(2025-1)

Background

Deadline for applications:
Agreement of the Academic Committee of the
Master's degree in Semiconductor Engineering
and Microelectronic Design (CAMSEMD)
Publication of the list of admitted candidates:

March 3rd to June 15th, 2025

July 8th, 2025

July 9th, 2025

Legislation applied

Academic regulation of undergraduate and master programs for the academic year 2025-2026, approved by the Governing Council of the UPC, agreement no. CG/2025/03/03 dated June 17th 2025, point 1.2 "Access to master studies."

Resolution

List of students admitted and excluded to the **Master's degree in Semiconductor Engineering and Microelectronic Design (SEMD)**

JULY 2025

ADMISSION LIST: 44 applications (sorted in alphabetical order)

	SURNAME AND NAME	ADMISSION QUALIFICATION	SPECIALISATION
1	ALISON, DOONEY	6,08	Specialization IN SEMICONDUCTOR ENGINEERING
2	ANTOLI CHERTO, LAIA	6,89	Specialization IN MICROELECTRONIC DESIGN
3	ARESTÉ CANTERA, JAUME	6,14	Specialization IN SEMICONDUCTOR ENGINEERING
4	BEL HAIMA EL JAZOULI, ABDELBASET	7,30	Specialization IN MICROELECTRONIC DESIGN
5	BERNARDI ALFAMA, NEHUEL	7,13	Specialization IN MICROELECTRONIC DESIGN
6	BERRETEAGA GAYUBO, MARKEL	6,79	Specialization IN MICROELECTRONIC DESIGN
7	BIROU, ALEXANDRA BIANCA	5,90	Specialization IN SEMICONDUCTOR ENGINEERING (*)
8	BUSQUETS VILLAGAS, JOEL	6,38	Specialization IN MICROELECTRONIC DESIGN
9	CEREZAL BILBAO, OIER	6,54	Specialization IN SEMICONDUCTOR ENGINEERING
10	COMAS MARIN, POL	7,10	Specialization IN MICROELECTRONIC DESIGN
11	CONDOR DOMO, ROBERTO PAUL	7,76	Specialization IN MICROELECTRONIC DESIGN
12	CORONEL GASTIAIN, ANDONI	6,99	Specialization IN SEMICONDUCTOR ENGINEERING
13	ÇUHADAR ÇUHADAR, YUSUF	5,60	Specialization IN SEMICONDUCTOR ENGINEERING (*)
14	CUI, YUXIANG	7,43	Specialization IN SEMICONDUCTOR ENGINEERING

Original signat per:

XAVIER ARAGONES CERVERA
Signat en: 09-07-2025 13:49:00
GMT+2





	SURNAME AND NAME	ADMISSION QUALIFICATION	SPECIALISATION
15	DIAZ CUESTA, PAU	7,93	Specialization IN MICROELECTRONIC DESIGN
16	FERNÁNDEZ RIUS, MARIO	6,71	Specialization IN SEMICONDUCTOR ENGINEERING
17	FLÓRIDO, GONÇALO	7,39	Specialization IN SEMICONDUCTOR ENGINEERING
18	FUGAROLAS MASÓ, BERTA	6,09	Specialization IN SEMICONDUCTOR ENGINEERING
19	GALITO FOLGUERA, ADRIK	7,20	Specialization IN MICROELECTRONIC DESIGN
20	GONZÁLEZ JIMENEZ, JOEL ULISES	7,88	Specialization IN MICROELECTRONIC DESIGN
21	GONZALEZ SEIJO, IBAN	7,00	Specialization IN SEMICONDUCTOR ENGINEERING
22	HERNÁNDEZ SOLARES, RUT NUEMI	6,36	Specialization IN MICROELECTRONIC DESIGN
23	JULIÀ MAIQUEZ, MARC	7,09	Specialization IN SEMICONDUCTOR ENGINEERING
24	LERMA SANCHEZ, SARA	7,25	Specialization IN MICROELECTRONIC DESIGN
25	LIU, DAXIANG	6,68	Specialization IN MICROELECTRONIC DESIGN
26	MARTÍ I MICHAVILA, JORDI	5,40	Specialization IN SEMICONDUCTOR ENGINEERING (*)
27	MUELLER, MARC	5,70	Specialization IN SEMICONDUCTOR ENGINEERING (*)
28	NABONA PALAU, JOSEP MARIA	7,56	Specialization IN MICROELECTRONIC DESIGN
29	NAULA FARINANGO, JOSE ANDRÉS	7,38	Specialization IN MICROELECTRONIC DESIGN
30	NEBOT PARRA, VICENÇ	6,87	Specialization IN MICROELECTRONIC DESIGN
31	PARES MELENDEZ, LUIS DAVID	6,42	Specialization IN MICROELECTRONIC DESIGN
32	PARRAGA ISIDRO, JHOEL EUSEBIO	6,33	Specialization IN SEMICONDUCTOR ENGINEERING
33	PRIETO BAILO, ENRIQUE JOSÉ	6,03	Specialization IN SEMICONDUCTOR ENGINEERING
34	RODRIGUEZ MELERO, JOSE	5,80	Specialization IN SEMICONDUCTOR ENGINEERING (*)
35	ROPERO SOBERBIO, MARC	6,96	Specialization IN MICROELECTRONIC DESIGN
36	RUIZ DE LA TORRE, SERGIO	7,00	Specialization IN MICROELECTRONIC DESIGN
37	SAUMELL HILL, POL	5,60	Specialization IN SEMICONDUCTOR ENGINEERING (*)
38	SENABRE I PRADES, GUILLEM	6,00	Specialization IN SEMICONDUCTOR ENGINEERING (*)
39	SHUVO, SHOWRAV SHARMA	7,90	Specialization IN MICROELECTRONIC DESIGN
40	SOTO CARBALLO, HUGO	8,08	Specialization IN SEMICONDUCTOR ENGINEERING
41	VLAD STEFAN, CIOLACU	8,59	Specialization IN MICROELECTRONIC DESIGN
42	WU, YUANHAO	7,33	Specialization IN MICROELECTRONIC DESIGN
43	YANG, BENXI	5,70	Specialization IN SEMICONDUCTOR ENGINEERING (*)
44	YOUNAS, MUHAMMAD HUSSNAIN	6,98	Specialization IN SEMICONDUCTOR ENGINEERING

(*) You have been admitted to the Specialization in Semiconductor Engineering, which is not your first choice.

If places become available, you may have the opportunity to enrol on the Specialization in Microelectronic Design, depending on your admission qualifications. If places are available after first week of September, we will contact you.

ADMINISTRATIVE PROCEDURE NOT SUCCESSFULLY COMPLETED: 1 application.

	ID	REASONS FOR EXCLUSION
1	51487976S	not accepted by Student

Original signat per:

XAVIER ARAGONES CERVERA
Signat en: 09-07-2025 13:49:00
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EXCLUSION LIST: 8 applications

	DNI/PASSAPORT	REASONS FOR EXCLUSION
1	EP3853038	inadequate degree for the right academic performance of master
2	CA33124SL	not sufficient curriculum
3	41578467X	inadequate degree for the right academic performance of master
4	48030092C	inadequate degree for the right academic performance of master
5	EL5275528	inadequate degree for the right academic performance of master
6	N05881512	inadequate degree for the right academic performance of master
7	39944986N	not sufficient curriculum
8	EA9539388	inadequate degree for the right academic performance of master

Hearing period

Starting from the day after the date of this resolution, you can present allegations to the competent body, which will be resolved within 10 days. Once this period is finished the resolution will be definitive, and appeal will be possible.

This is notified, adding that this resolution does not exclude the administrative procedures, and is subject to lodging of an appeal, according to the provisions of Articles 114 and 115 of Law 4/1999 of 13 January, amending the Law 30/1992 of 26 November on the legal regime of Public Administrations and the common administrative procedures: Appeal to the Rector of the Universitat Politècnica de Catalunya (UPC) within one month from the day after the reception of this notification. All this without excluding any other appeal you might consider under the provisions of art. 58.2 of Law 30/1992 of 26 November.

Signature of the competent authority for the resolution and notification

The responsible of the Master's degree in Semiconductor Engineering and Microelectronic Design (SEMD).

Xavier Aragonès Cervera
Barcelona July 9th, 2025

Original signat per:

XAVIER ARAGONÉS CERVERA
Signat en: 09-07-2025 13:49:00
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Motiu de la resolució

LLISTAT D'ADMESOS al Màster Universitari en Enginyeria de Semiconductors i Disseny Microelectrònic (SEMD)

Centre

ESCOLA TÈCNICA SUPERIOR DE TELECOMUNICACIONS DE BARCELONA (ETSETB)

Curs acadèmic
2025/2026

Quadrimestre
Q1 (2025-1)

Antecedents

Termini de presentació de sol·licituds:

3 Març al 15 Juny, 2025

Acord de la Comissió Acadèmica del

Màster Universitari en Enginyeria

de Semiconductors i Disseny Microelectrònic (SEMD)

8 de Juliol 2025

Publicació del llistat d'admesos:

9 de Juliol 2025

Fonaments de dret

Normativa Acadèmica dels estudis de Grau i Màster de la UPC (NAGRAMA) 2025-2026 aprovada per acord CG/2025/03/03 de 17 de juny de 2025, del Consell de Govern de la UPC punt 1.2 "Accés als estudis de màster".

Resolució

Llistat d'estudiants admesos i estudiants exclosos per accedir als estudis de **Màster Universitari en Enginyeria de Semiconductors i Disseny Microelectrònic (SEMD)**

JULIOL 2025

LLISTAT D'ADMESOS: 44 candidatures (ordenats per ordre alfabètic)

	COGNOMS I NOM	QUALIFICACIÓ D'ADMISSIÓ	ESPECIALITAT (*)
1	ALISON, DOONEY	6,08	Specialization IN SEMICONDUCTOR ENGINEERING
2	ANTOLI CHERTO, LAIA	6,89	Specialization IN MICROELECTRONIC DESIGN
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(*) Has estat admès a l'Especialització en Enginyeria de Semiconductors, que no és la teva primera opció.

Si queden places disponibles, és possible que tinguis l'oportunitat de matricular-te a l'Especialització en Disseny Microelectrònic, en funció de la qualificació d'Admissió. Si queden places disponibles després de la primera setmana de setembre, ens posarem en contacte amb tu.

Original signat per:

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FINALITZADES: 1 candidatura.

	ID	Motiu finalització
1	51487976S	No acceptada per l'estudiant/a

LLISTAT D'EXCLOSOS: 8 candidatures.

	DNI/PASSAPORT	MOTIU D'EXCLUSIÓ
1	EP3853038	titulació d'accés no adient pel correcte aprofitament del màster
2	CA33124SL	currículum no suficient
3	41578467X	titulació d'accés no adient pel correcte aprofitament del màster
4	48030092C	titulació d'accés no adient pel correcte aprofitament del màster
5	EL5275528	titulació d'accés no adient pel correcte aprofitament del màster
6	N05881512	titulació d'accés no adient pel correcte aprofitament del màster
7	39944986N	currículum no suficient
8	EA9539388	titulació d'accés no adient pel correcte aprofitament del màster

Tràmit d'audiència

A partir de l'endemà de la data de la present, podeu presentar al·legacions davant l'òrgan competent per resoldre, en un termini màxim de 10 dies. Exhaurit l'esmentat termini la present resolució esdevindrà ferma i possible objecte d'interposició de recursos.

Contra aquesta resolució que no esgota la via administrativa, es podrà interposar recurs d'alçada davant aquest mateix òrgan o davant el rector, en el termini d'un mes, a comptar des de l'endemà de la notificació d'aquesta resolució, d'acord amb l'article 121 i l'article 122 de la Llei 39/2015, d'1 d'octubre, del procediment administratiu comú de les administracions públiques.

Signatura de l'òrgan competent per resoldre i notificar

El responsable del **Màster Universitari en Enginyeria de Semiconductors i Disseny Microelectrònic (SEMD)**

Xavier Aragonès Cervera
Barcelona, 9 de juliol 2025

Original signat per:

XAVIER ARAGONÉS CERVERA
Signat en: 09-07-2025 13:49:00
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